

ABSTRACT

A solar cell is configured to include: a substrate (21); a conductive film (22) formed on the substrate (21); a compound semiconductor layer (23) formed on the conductive film (22), including a p-type semiconductor crystal containing an element of Group Ib, an element of Group IIIb, and an element of Group VIb; a n-type window layer (24) formed on the compound semiconductor layer (23), having apertures (29); and a n-type transparent conductive film formed on the n-type window layer (24) and on portions of the compound semiconductor layer (23) at the apertures of the n-type window layer (24). The compound semiconductor layer (23) includes high-resistance parts (23B), in portions of the compound semiconductor layer (23) in the vicinity of a surface thereof on a side opposite to the conductive film (22), and the high-resistance parts (23B) contain a n-type impurity doped in the p-type semiconductor crystal. The high-resistance parts (23B) are located under the apertures (29) of the n-type window layer (24), respectively.